T PACKARD LGL FCOLL 970 898 7247 P.1

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

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Blaine D. Gaither et al

Confirmation No.: 2685

Application No.: 09/704176

Fort Collins, Colorado 80527-2400

Examiner: Elmore, Stephen C

Filing Date:

Oct 31, 2000

Group Art Unit: 2186

Title:

Cache Coherence Protocol For A Multiple Bus Multiprocessor System

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450



PETITION FOR EXTENSION OF TIME

Sir:

In an Office Action mailed on	6-23-2004	, on the above-identified U.S. Patent
application, a shortened statutory per	riod of 3	months was set for response. In accordance
with 37 C.F.R. 1.136(a), applicant(s)	hereby request(в) а:
(X) one month	1	
() two monti	าร	·
() three mon	ths	
() four mont	hs	
time extension so that the period for	response to the	Office Action expires on 10-23-2004

Authorization to charge the fee required by 37 CFR 1.17 to Deposit Account 08-2025 appears in the enclosed transmittal letter. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

Typed Name: Cathi Christensen

Signatura:

Respectfully submitted,

Blaine D. Gaither et al

Assessment INC MANAGES

Attorney/Agent for Applicant(s)

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Date: 10-22-2004

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PAGE 4/7 * RCVD AT 10/22/2004 3:01:11 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-2/0 * DNIS:7457239 * CSID:970 898 7247 * DURATION (mm-ss):02-24